

# INDRAPRASTHA INSTITUTE OF INFORMATION TECHNOLOGY DELHI

(A State University established by Govt. of NCT of Delhi)

Date: 16/09/2024

**Subject:** Applications are invited for **PhD** position under Visvesvaraya PhD Scheme for MeiT sponsored Project on *NavISense: Design and Prototype of NavIC Signal Processing Accelerator on Heterogeneous System-on-Chip for Remote Sensing* at IIIT Delhi

<b>Post</b>	PhD
<b>PhD Fellowship</b>	<ul style="list-style-type: none"><li>• Fellowship @ Rs. 38,750/- per month for the 1st and 2nd years</li><li>• Fellowship @ Rs. 43,750/- per month for the 3rd, 4th, and 5th years of PhD</li><li>• Reimbursement of Rent (RoR) (Class Z @ 24%)</li><li>• Research Contingency Grant of Rs. 30,000 per year</li></ul>
<b>Essential Qualifications</b>	<ul style="list-style-type: none"><li>• Bachelor's and/or Master's degree in CSE/ECE with experience in one of the following:<ul style="list-style-type: none"><li>➤ VLSI and Embedded Systems</li><li>➤ Wireless Communications and Signal Processing</li></ul></li><li>• CGPA should be more than 7.</li><li>• Good interpersonal and communication skills (both oral and written)</li></ul>
<b>Desirable Qualifications and Experience Duration</b>	<ul style="list-style-type: none"><li>• Proficient in one of the following programming languages:<ul style="list-style-type: none"><li>➤ HDL: Verilog/VHDL with hands-on experience with FPGA/SoC platforms</li><li>➤ Matlab or Python with prior experience in research projects</li></ul></li><li>• Prior publications or research experience is desired</li></ul>
<b>Duration</b>	3-4 years
<b>Application Deadline</b>	Oct. 1, 2024
<b>Application Process</b>	<ul style="list-style-type: none"><li>• Please submit your applications here: <a href="https://forms.gle/wKc1js6RQGreCiUp7">https://forms.gle/wKc1js6RQGreCiUp7</a></li></ul>

Regards,  
Dr. Sumit J Darak, Dr. Sanat Biswas and Dr. Shobha Sundar Ram  
ECE, IIIT Delhi