

**INDRAPRASTHA INSTITUTE OF INFORMATION TECHNOLOGY DELHI**  
(A State University established by Govt. of NCT of Delhi)

**Date: 19/01/2025**

**Subject:** Applications are invited for **PhD** and **Research Engineer** position at Algorithms to Architecture Lab, IIIT Delhi

<b>Post</b>	PhD
<b>PhD Fellowship</b>	<ul style="list-style-type: none"><li>● Fellowship: Rs. 60000/- per month</li><li>● Research Contingency Grant of Rs. 2,50,000</li></ul>
<b>Research Engineer</b>	<ul style="list-style-type: none"><li>● Salary: Rs. 30000/- per month</li></ul>
<b>Essential Qualifications</b>	<ul style="list-style-type: none"><li>● Bachelor's and/or Master's degree in CSE/ECE with experience in one of the following:<ul style="list-style-type: none"><li><input type="checkbox"/> VLSI and Embedded Systems</li><li><input type="checkbox"/> Wireless Communications and Signal Processing</li></ul></li><li>● CGPA should be more than 7.</li><li>● Good interpersonal and communication skills (both oral and written)</li></ul>
<b>Desirable Qualifications and Experience Duration</b>	<ul style="list-style-type: none"><li>● Proficient in one of the following programming languages:<ul style="list-style-type: none"><li><input type="checkbox"/> Hands-on experience with FPGA/SoC platforms</li><li><input type="checkbox"/> Matlab or Python with prior experience in research projects</li></ul></li><li>● Prior publications or research experience is desired</li></ul>
<b>Duration</b>	3-4 years based on the performance
<b>Application Deadline</b>	March 1, 2025 (Interviews will be conducted on weekly basis starting from Jan. 25, 2025)
<b>Application Process</b>	<ul style="list-style-type: none"><li>● Please submit your applications here: <a href="https://forms.gle/wKc1js6RQGreCiUp7">https://forms.gle/wKc1js6RQGreCiUp7</a></li></ul>

Regards,  
Dr. Sumit J Darak, Dr. Sanat Biswas and Dr. Shobha Sundar Ram  
ECE, IIIT Delhi